



PATENT NUMBER

6380559

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U.S. UTILITY Patent Application

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O.I.P.E.
CJH
SCANNED Q.A.

PATENT DATE
APR 30 2002

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/585427	F	257	59	2811 2826	TRAN, M.

APPLICANTS
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TITLE
Thin film transistor array substrate for a liquid crystal display
and a method for fabricating the same

PTO-2040
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
257	59	257	72		
INTERNATIONAL CLASSIFICATION	349		106	143	158
H01L 29/04					
G02F 1/136					
G02F 1/1335					

Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
74	76	45	17	1	
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.			NOTICE OF ALLOWANCE MAILED Minh Leer Tran Primary Examiner 1/2/02		
			ISSUE FEE Amount Due 12/01 Date Paid 215-02		
			ISSUE BATCH NUMBER 1280-02 215-02 1/3/02		
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